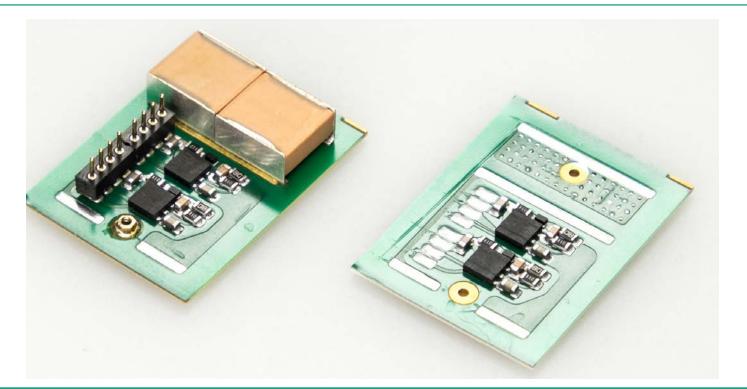
# **FRAUNHOFER IZM**

# Impact of electromobility on power electronic modules

Einfluss der Elektromobilität auf Leistungselektronik-Module

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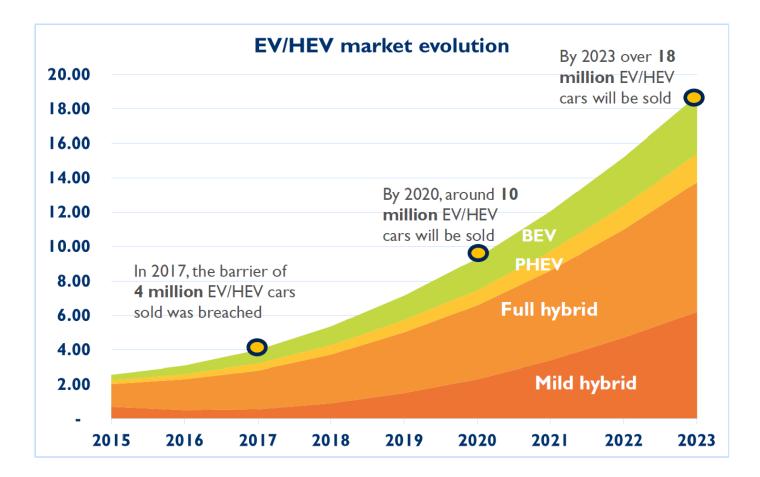




# Agenda

- EV/HEV market evolution
- Impact on power electronic modules
  - > Highly reliable, miniaturized, new materials and new interconnects
- Thoughts @ Fraunhofer IZM
- Possibilities for laser-based Interconnects
  - Converter (external) connectors -> Laserwelding
  - Converter (external) connectors / Heavy wire bonds -> Laserbonding
- Battery interconnection

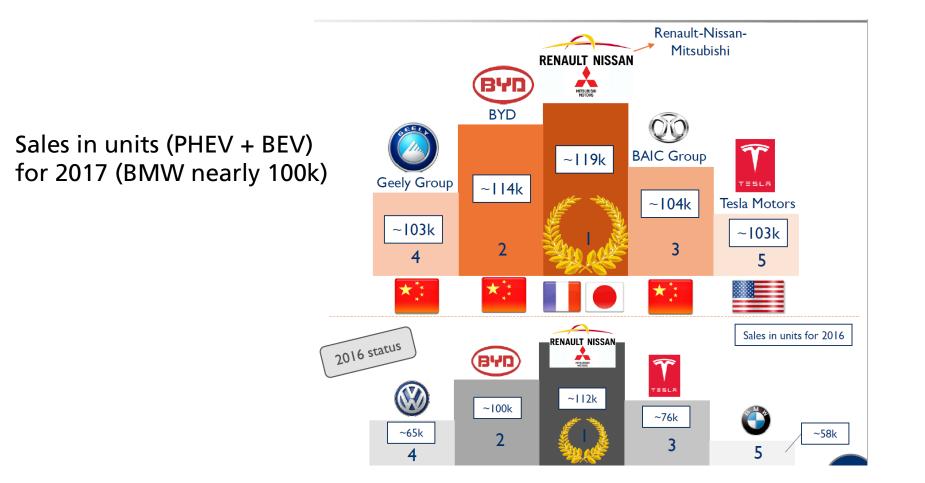




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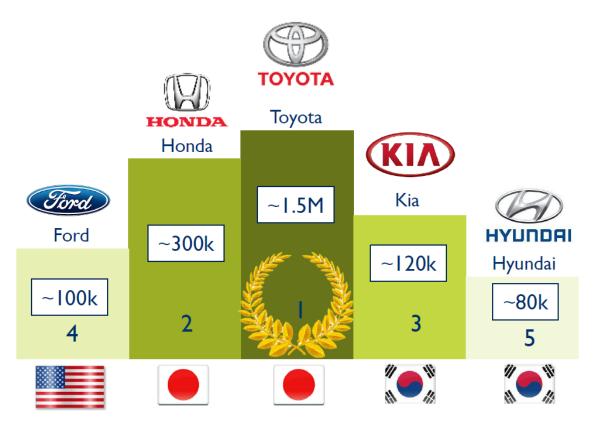


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Sales in units (Full HEV + Mild HEV) for 2017 (BMW nearly 100k)



There is no doubt that Toyota will remain the big leader of the hybrid market, as by 2030 they forecast to sell about 4.5 million hybrids worldwide.

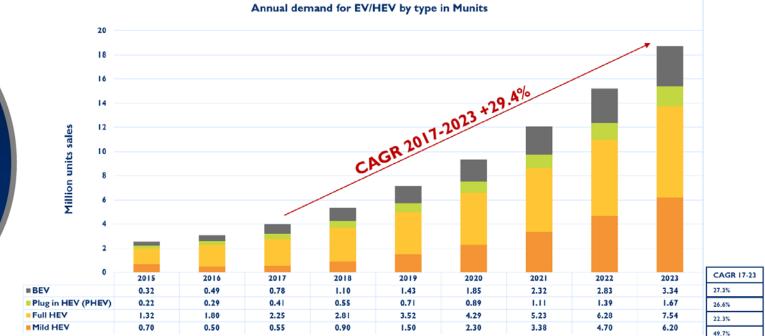
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Vehicle unit sales 2015-2023

The mild-hybrid segment will experience an impressive increase of 49.7% between 2017-2023, driven by the 48-volt system.

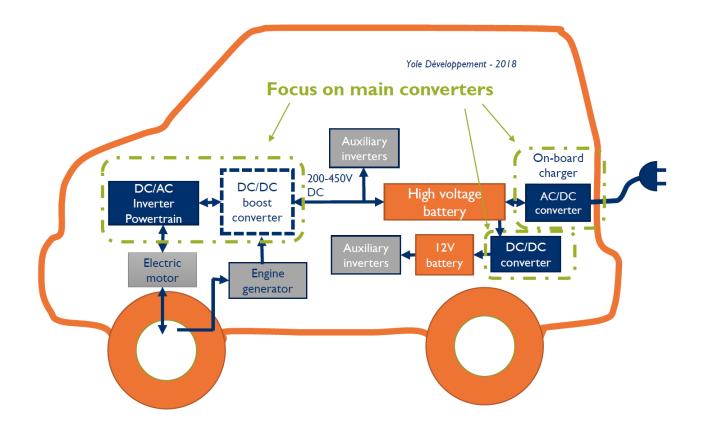


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#### Power Electronics and Automotive Application

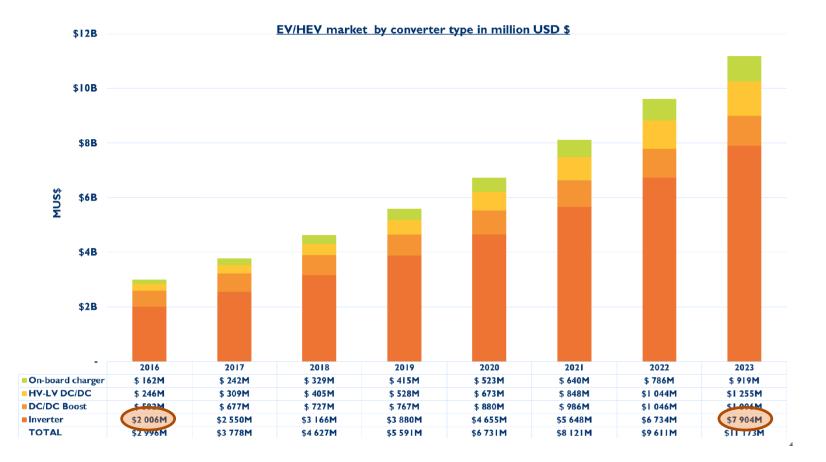


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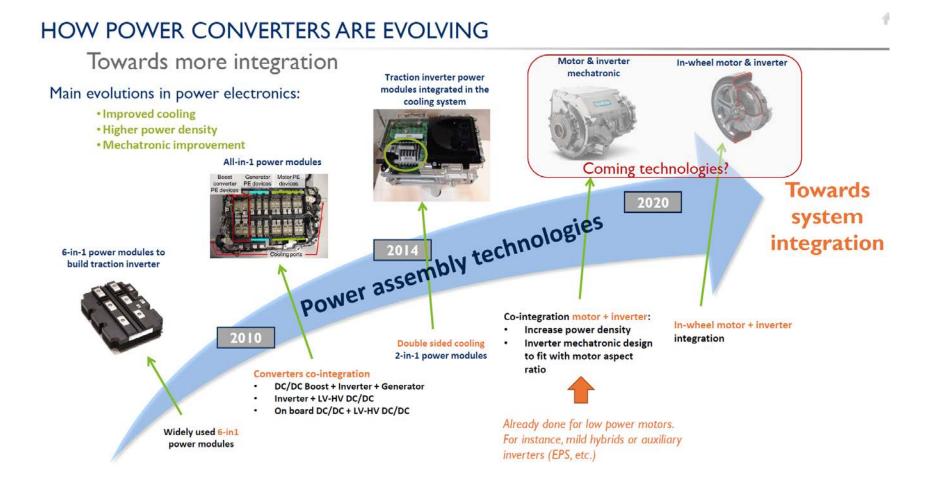
#### EV/HEV converter market



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EconoPACK<sup>™</sup>+ Modul (SixPACK up to1700 V / 450 A) Standard modules reliability problems Wire bond fatigue Solder die and substrate attach creep/fatigue **Courtesy of Infineon (Eupec)** connector power\chips wire bonds encapsulati housing Die attach DCB substrate base plate (depends on application) **DCB** attach

Schneider-Ramelow, M; et al.: Technologies and Trends to Improve Power Electronic Packaging. 44<sup>th</sup> IMAPS International Symposium on Microelectronics (2011).

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Weak points and solutions in power module packaging

Field	Kind of failure	Approach / solution	
DCB-Cu on Cu base plate	Solder cracking (creep/fatigue)	<ul> <li>Other base plate material (AlSiC, AlSi, Al-Graphit, Cu/Mo/Cu, Cu/W/Cu)</li> <li>spring/pressure contact, other TIM</li> <li>no base plate or no DCB</li> </ul>	
Al wedge bond on chip	Al fatigue	<ul> <li>Ribbon (Cu/Al), Cu or AlX wire bonding</li> <li>Sintering/soldering of formed parts</li> <li>double-sided cooling/contacts</li> </ul>	
Chip on DCB- Cu	Solder cracking (creep/fatigue)	<ul> <li>Ag or TLP sintering</li> <li>Nanofoil reactive joining</li> <li>Pressure contact</li> </ul>	
DCB: Cu on ceramic	Ceramic cracking	<ul> <li>High strength ceramic</li> <li>Dimples or thin Cu</li> <li>DAB or other substrates</li> </ul>	
Cu to Cu connector	Cracks in solder or ceramic	<ul> <li>Welded bonds to external connectors</li> <li>Pressure contacts</li> </ul>	

Schneider-Ramelow, M; et al.: Technologies and Trends to Improve Power Electronic Packaging. 44<sup>th</sup> IMAPS International Symposium on Microelectronics (2011). (adjusted Febr. 2019)

Prof. M. Schneider-Ramelow

11 Feb. 2018

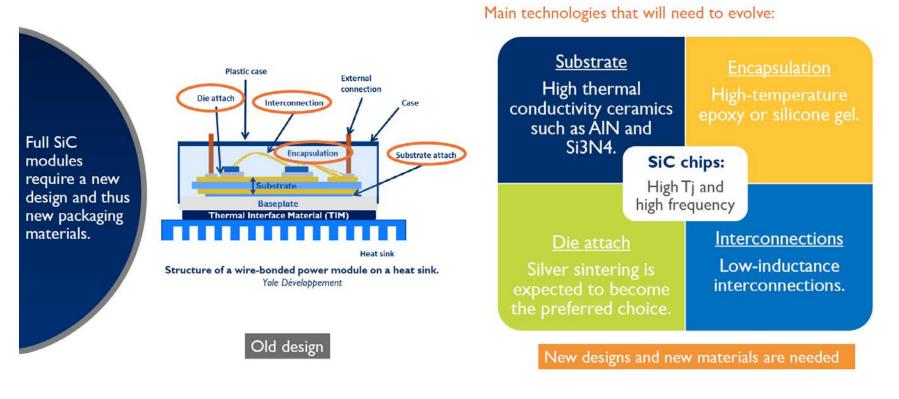
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# Impact on power electronic modules: new semiconductor materials like SiC

#### FULL SIC MODULE PACKAGING

Impact of SiC chips on choice of packaging materials

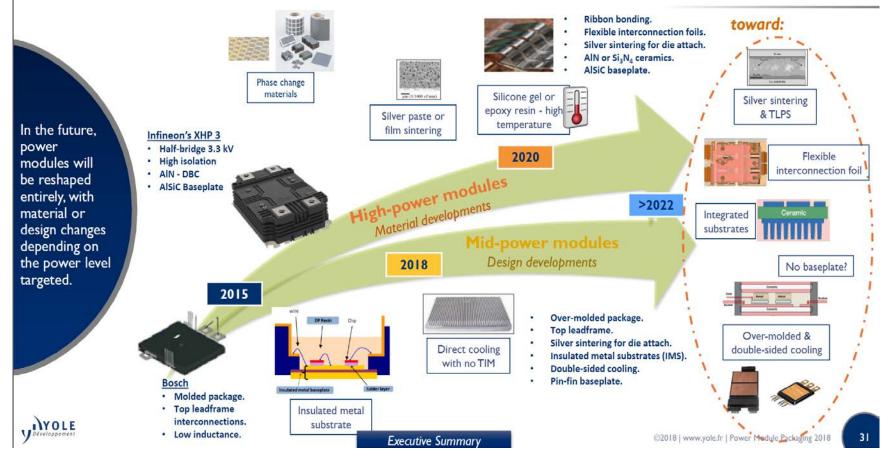


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Roadmap of power module packaging design



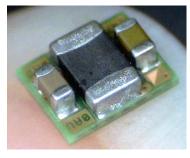
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#### Is Integration the Next Milestone for Power Electronics?

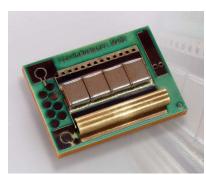
Three examples giving an idea on future development



🔱 Texas Instruments



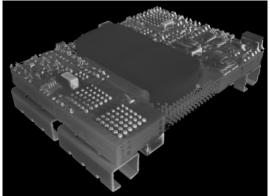
PCB with embedded semiconductors and SMD passives





Ultra Low Inductance modules in Embedding on DCB technology





Multilayer PCB including coil windings, overmolded

Hoene, E; Fraunhofer IZM 2016

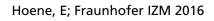
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#### What makes sense to be integrated?



- Low power DC/DC converter with all necessary active and passive components
- Prepared for PCB mounting
- The semiconductor package is at the same time substrate for passives
- -> reduction of manufacturing processes and space



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- Switching cell in the package including driver, primary DC link and safety insulation
- Thermal interface on the one side, electrical interface on the other
- Electromagnetically optimized for fast switching

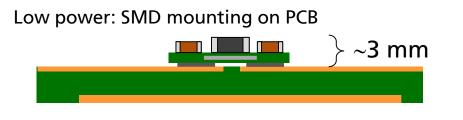
-> Integration of the "heart" of the circuit in a package



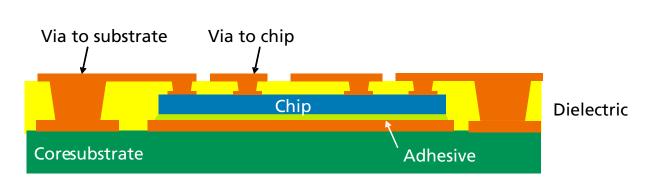
- High power DC/DC converter with all necessary active and passive components
- Double sided cooling
- Robust, thermally conducting and electrically insulating encapsulation
- -> space saving and robust

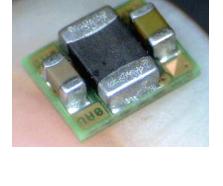


#### What is desired by the users?

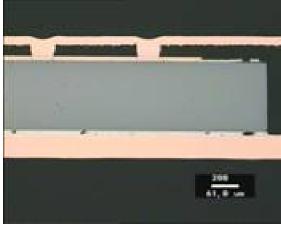


- 2 component layers integrated: chips and SMD
- Cooling by Master PCB, losses < 1W</li>
- Handling like a SMD component





Size <1cm<sup>2</sup>



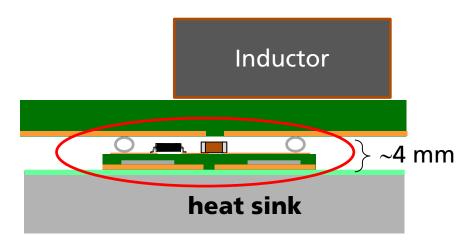
#### Hoene, E; Fraunhofer IZM 2016

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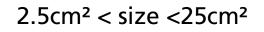


# Thoughts @ Fraunhofer IZM What is desired by the users?

Higher power requiring a heat sink and (safety) insulation







- "Switching Cell in Package"
- Thermal interface on the one side including (safety) insulation against heat sink
- 2 component layers integrated: Chips and SMD
- Electric interface on the other side by springs or soldering
- Peripherals on the module

#### Hoene, E; Fraunhofer IZM 2016

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# Thoughts @ Fraunhofer IZM How will power electronics look like in future?

1<sup>st</sup> answer: Flat

Thermal path must be kept short and is the No.1 bottle neck



- Stacked power modules with both side cooling by Toyota
- Safety insulation by ceramic substrates
- Assembly with thermal grease (performance bottle neck)

Hoene, E; Fraunhofer IZM 2016

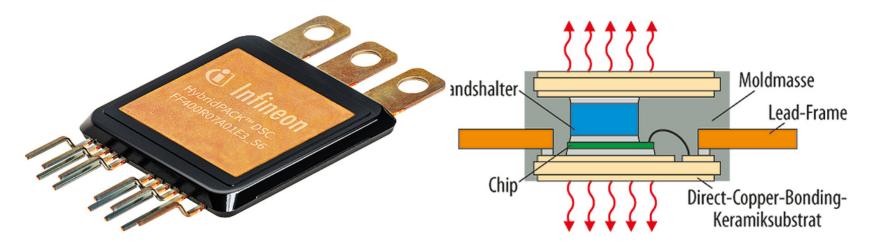
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#### How will power electronics look like in future?

#### 1<sup>st</sup> answer: Flat

 Example for highest performance package: double side cooled semiconductor, ceramic substrates as insulation, only metallic interconnects



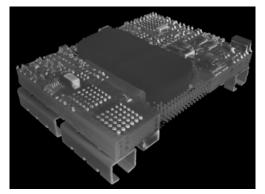


#### How will power electronics look like in future?

2<sup>nd</sup> answer: It will use Heterogeneous Integration Technologies

Power electronics cannot be monolithically integrated, it needs:

- Semiconductors (volume ~3%, CTE 2-4)
- Inductors -> ferrite (volume ~20%, CTE 10, pressure sensitive)
- Capacitors -> ceramics (volume ~10%, CTE 10, piezoelectric)
- Conductors -> copper (~15%, CTE 17)
- Control, drivers, sensors (~12%)
- Insulation and unused volume (~40%, shrinkage, non homogeneous CTE)



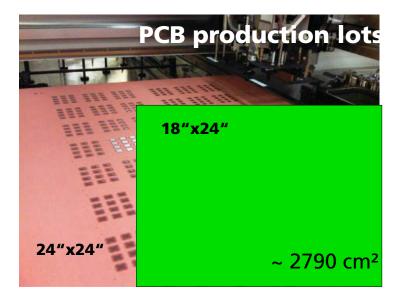
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#### How will power electronics look like in future?

3<sup>rd</sup> answer: PCB based technologies are the basis for integration

- Soldering components onto PCBs is the common technology applicable to all types of components
- Especially transformers can be designed using PCB windings
- High interconnect numbers without extra costs
- Big production lots, established low cost technology



#### Hoene, E; Fraunhofer IZM 2016

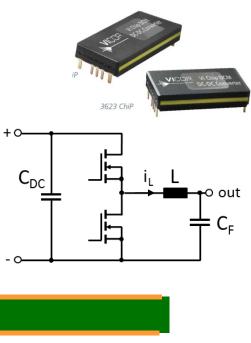
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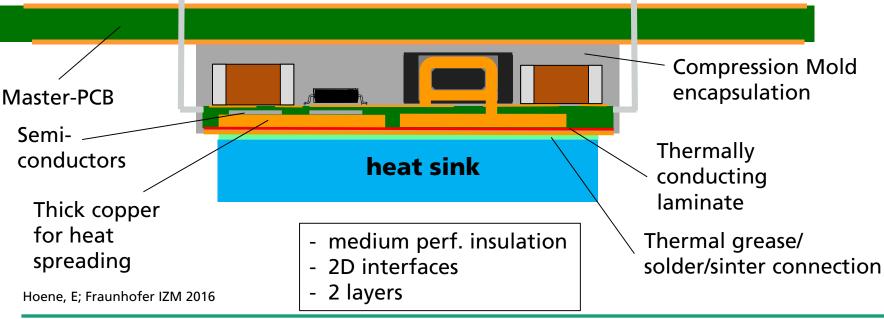


#### Which types of devices make sense?

Mid Power Power System in Package with insulation: Switching Cell with output inductor

 Integration of no.1 performance defining components: Switching cell incl. drivers, output inductor, DC and filtering capacitors, PWM and protection

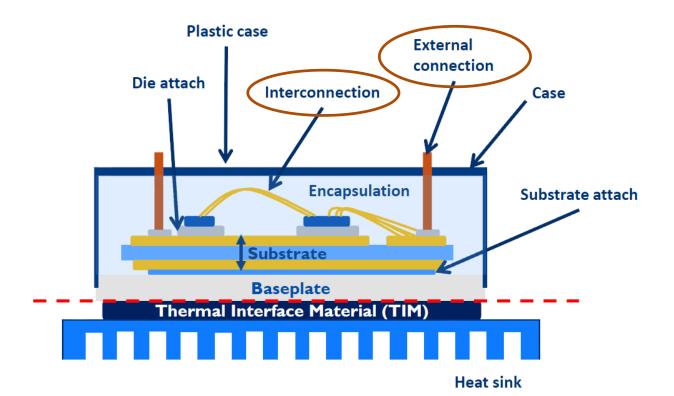




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Possible application for laser technologies?



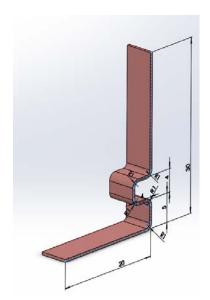


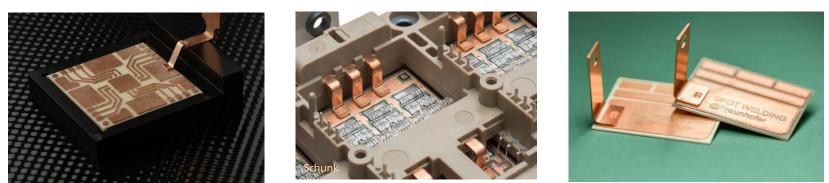
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# **Possibilities for laser-based Interconnects**

Converter (external) connectors

- US welding [Kido 2010]
- Laser beam welding [ILT 2013/14]
- Resistance welding [Waltrich 2015]





[2] Kido, K.; et. al.: "Development of Copper-Copper Bonding by Ultrasonic Welding for IGBT Modules," 34th International Electronic Manufacturing Technology Symposium (IEMT 2010), pp. 1-5.
[3] Mehlmann, B.; et.al.: "Laserstrahlschweißen elektrischer Anschlüsse an DAB-Substrate", ILT Jahresbericht13/14]
[4] Waltrich, U.; et. al.: "Bonding Copper Terminals onto DBC Substrates of Power Modules by Resistance Projection Welding," 5th International Electric Drives Production Conference (EDPC 2015)

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# **Possibilities for laser-based Interconnects**

Converter (external) connectors / Heavy wire bonds -> Laserbonding



# For high current interconnection of power chips and housing components

Courtesy of F&K Delvotec

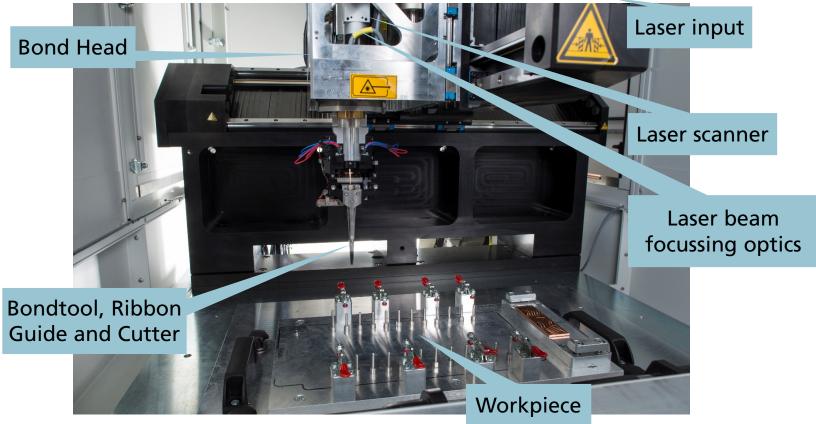


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# **Possibilities for laser-based Interconnects**

#### Converter (external) connectors / Heavy wire bonds -> Laserbonding

The bond head with Laser

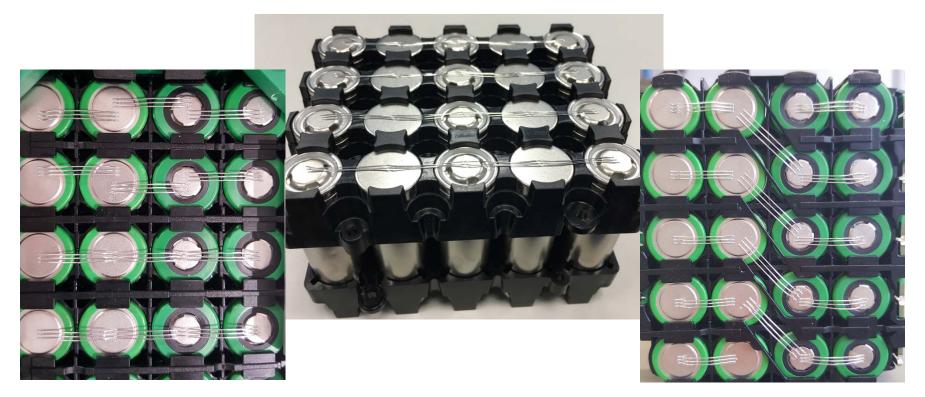


Courtesy of F&K Delvotec



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#### Standard heavy wire bonds



Connected with 3 aluminum wires of 400  $\mu m$  each

Courtesy of F&K Delvotec

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#### Standard heavy wire bonds



Each cell connected to a joint connector mask by 400 µm Al wire

Courtesy of F&K Delvotec





#### Laserbonding for battery cells



Use laser instead of ultrasonic power

Advantages

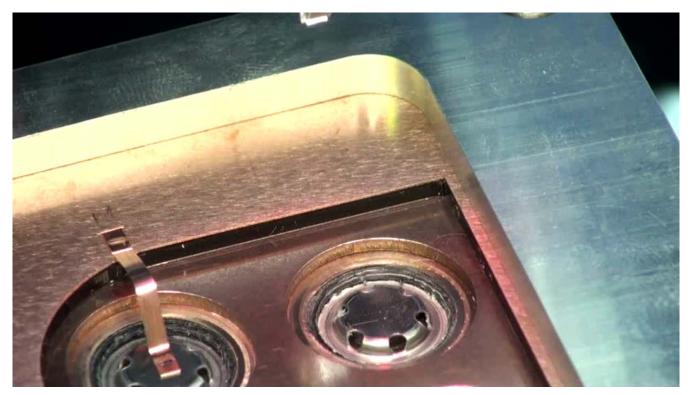
- Usage of cheap copper ribbons
- No problems with vibrations
- Welding geometry is adjustable
- Simple part clamping
- Higher tolerance for variations in surface conditions

Courtesy of F&K Delvotec



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#### Process for battery production



Laser bonding on plus terminal of cylindrical cell (type 18650) with copper ribbon (2 mm x 0.3 mm)

Courtesy of F&K Delvotec



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#### Process – What is possible?

#### US-Laserbonding by F&K Delvotec Bondtechnik GmbH

Ribbon	Al	Cu	Ni
Surface			
Al	+	o/-	
Al-Diecast	+/o		
Cu	0	+	+*
CuSn6		+	+*
CuFe2P		+	+*
ENIG on Al	+		
ENIG on Cu	0	+	+*
Steel	-	+	+
Steel (Ni)		+	+

\* Unaudited, theoretical statement. Depending on the product, each process needs to be qualified.

- Ribbon thickness up to 0,5 mm
- Ribbon width from 0,5 mm up to 2,0 mm
- Design Rules:
  - Ribbon should be of equal thickness as the bond pad or at most 50% thicker
  - Depending on ribbon width, the tool requires a space of about 4 x 4 mm<sup>2</sup>
  - Surface and base of the second bond must permit cutting the ribbon

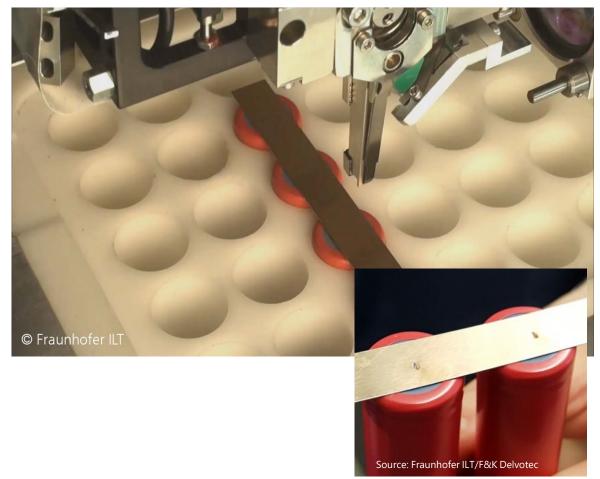
+...recommended O...depending on application -...not recommended

Courtesy of F&K Delvotec



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#### Laser tab bonding of battery terminal



Advantages

- Process can bridge gaps between sheet material and battery cell
- Controlled deformation of sheet material to minimize the gap
- The laser spot is exactly where it is supposed to be (focal position to the surface and on battery tab)
- Time between two welds: < 0.7 s</p>

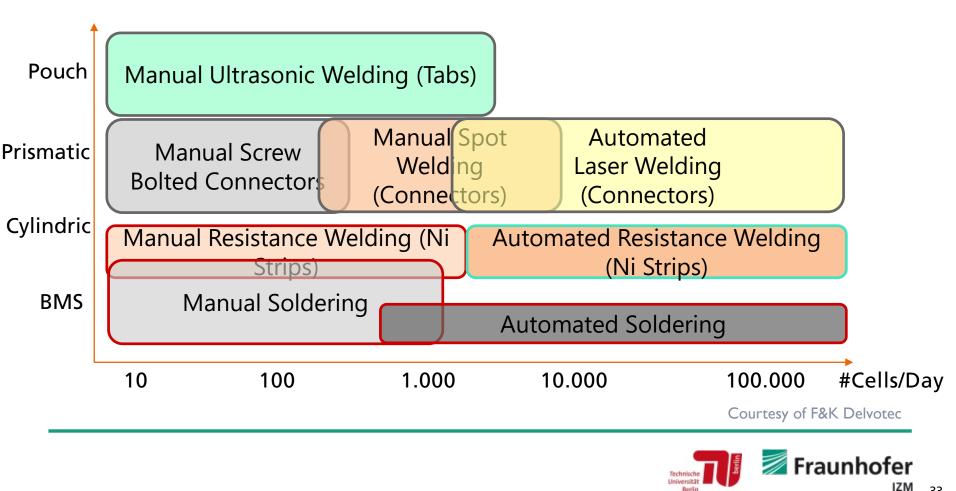
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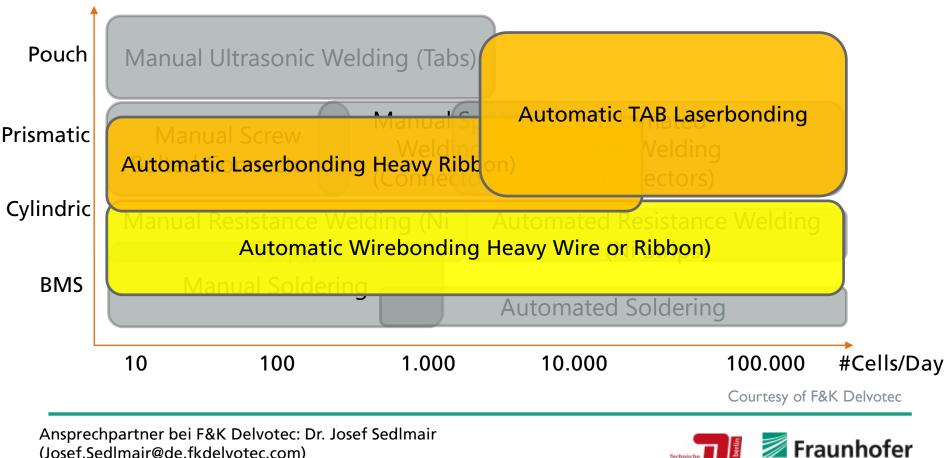
# A zoo of interconnecting technologies

#### Zellformate



### Wire- and Laser Bonder are very versatile

#### Zellformate



(Josef.SedImair@de.fkdelvotec.com)

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# Thank you for your attention!

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